## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

al No.:

10/692,683

Art Unit:

2676

cet No.:

10/27/2003

Examiner:

HSU, Joni

USP3033T/TP220-BIT

Method for an Image Reducing Processing Circuit

## PETITION FOR EXTENSION OF TIME UNDER 37 C.F.R. 1.136

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir.:

It is respectfully requested that an Extension of Time for the period indicated below be granted in accordance with the provisions of 37 C.F.R. 1.136 to take the action required in the application identified in caption, as reflected by the papers submitted.

☐First Month ☐Second Month	\$120.00 (\$60.00)* \$450.00 (\$225.00)*
⊠Third Month	\$1,020.00 (\$510.00)*
☐Fourth Month	\$1,590.00 (\$795.00)*
Fifth Month	\$2,160.00 (\$1,080.00)*
*Small Entity	TOTAL: \$ 510.00
A check in the amount of the above total fee is attached.  This amount is believed to be correct; however, the Commissioner is hereby authorized to charge any deficiency or credit any overpayment to Deposit Account No. 502111. A duplicate copy of this letter is attached.	
Please charge any fee which copy of this letter is attack	ich may be required to Deposit Account No A duplicate hed.

Respectfully submitted,

By:

Raymond Y. Chan Reg. No.: 37,484

108 N. Ynez Ave., Suite 128 Monterey Park, CA 91754

## **CERTIFICATE OF MAILING**

I certify that this correspondence will be deposited with the United States Postal Service as First Class Mail in an envelope addressed to: "Commissioner for Patents, P.O. Box-1450, Alexandria, VA 22313-1450" on the date below.

Date of Deposit

Signature of Registered Representative

07/18/2005 MBERHE

00000035 10692683

510.00 OP

01 FC:1253